**Lab Preparation**

1. PF2 and ADCValue is attached to Timer\_0A interrupt, PF1 runs in main()
   1. The DCW statements hold the port memory addresses, such as Port F’s direction address is 0x4002.5400
   2. The entire subroutine takes ~125ns (include the branch) but just toggling PF1 takes ~100ns
   3. The first LDR loads the base address for port F into R0, and the second LDR loads the PORTF\_DIR\_R address into R0
   4. Instead of loading Port F’s base address, just directly link to the PORTF\_DIR\_R address to save an LDR call
   5. No, a critical section is not created since PF2 is accessed in a bit-specific method (it was defined at the top).
2. Made Timer1.c
3. Made time\_dump[1000] and data\_dump[1000] arrays
   1. Check that extern volatile uint32\_t time works in both Timer1.c and ADCTestMain.c